

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(driver driving) near3 (writ\$5 record\$5) and (output) same (odd) and ("360,60,61,66,67,68".ccls. and 324/210,212.ccls.)	USPAT	OR	OFF	2005/11/11 10:16
L2	0	(driver driving) near3 (writ\$5 record\$5) and (output) same (characteristic\$5) and ("360,60,61,66,67,68".ccls. and 324/210,212.ccls.)	USPAT	OR	OFF	2005/11/11 10:16
L3	0	(driver driving) near3 (writ\$5 record\$5) and (resist\$5 impedance) same (characteristic\$5) and ("360,60,61,66,67,68".ccls. and 324/210,212.ccls.)	USPAT	OR	OFF	2005/11/11 10:17
L4	0	(driver driving) near3 (writ\$5 record\$5) and (resist\$5 impedance) same (characteristic\$5) and ("360,60,61,66,67,68".ccls. and 324/210,212.ccls.)	USPAT	OR	ON	2005/11/11 10:17
L5	0	(driver driving) near3 (writ\$5 record\$5) and (resist\$5 impedance) same (characteristic\$5) and ("360,60,61,66,67,68".ccls. and 324/210,212.ccls.)	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 10:18
L6	0	(driver driving h-bridge (h near bridge)) near3 (writ\$5 record\$5) and (resist\$5 impedance) same (characteristic\$5) and ("360,60,61,66,67,68".ccls. and 324/210,212.ccls.)	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 10:18
L7	0	(driver driving h-bridge (h near bridge)) near3 (writ\$5 record\$5) and (resist\$5 impedance) same (characteristic\$5) and ("360,60,61,66,67,68".ccls. and 324/210,212.ccls.)	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 10:18
L8	0	(driver driving h-bridge (h near bridge)) near3 (writ\$5 record\$5) and (resist\$5 impedance) same (characteristic\$5) and (360/60,61,66,67,68.ccls. and 324/210,212.ccls.)	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 10:21
L9	2	10/824096	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 10:24

L10	10	venca near alessandro	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 10:26
L12	64	(roberto near alini).inv.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 10:41
L13	11	(baris near posat).inv.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 11:56
L14	5469	360/60,61,66,67,68.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 11:56
L15	538	l14 and (driver driving) near3 (writ\$5 record\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 12:12
L16	119	l15 and (first one) same (second other) same (current bias\$5) same source	USPAT	OR	ON	2005/11/11 12:07
L17	6	l16 and interconnect	USPAT	OR	ON	2005/11/11 11:58
L18	7	l15 and (first one) same (second other) same (current bias\$5) same half	USPAT	OR	ON	2005/11/11 12:12
L19	1	l15 and (first one) same (second other) same (current bias\$5) same half same (magnitude)	USPAT	OR	ON	2005/11/11 12:12
L20	404	324/210,212.ccls.	USPAT	OR	ON	2005/11/11 12:12
L21	27	l20 and (driver driving) near3 (writ\$5 record\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/11 12:12
L22	1	l21 and (first one) same (second other) same (current bias\$5) same half	USPAT	OR	ON	2005/11/11 12:13

L23	5	I21 and (current bias\$5) same (magnitude)	USPAT	OR	ON	2005/11/11 12:17
L24	384	360/60,61,66,67,68.ccls. and (current bias\$5) same (magnitude)	USPAT	OR	ON	2005/11/11 12:18
L25	3	360/60,61,66,67,68.ccls. and (current bias\$5) same (magnitude) near5 half	USPAT	OR	ON	2005/11/11 12:20
L26	0	360/60,61,66,67,68.ccls. and (current bias\$5) same (magnitude) near5 halved	USPAT	OR	ON	2005/11/11 12:18
L27	2	360/60,61,66,67,68.ccls. and (current bias\$5) same (magnitude) near5 divided	USPAT	OR	ON	2005/11/11 12:18
L28	189	360/60,61,66,67,68.ccls. and (current bias\$5) same (half halves halved)	USPAT	OR	ON	2005/11/11 12:21
L29	74	360/60,61,66,67,68.ccls. and (current bias\$5) same (half halves halved) same (impedance resist\$5)	USPAT	OR	ON	2005/11/11 12:42
L30	7	360/60,61,66,67,68.ccls. and (current bias\$5) same (half halves halved) same (impedance resist\$5) same characteristic	USPAT	OR	ON	2005/11/11 13:46
L31	237	360/60,61,66,67,68.ccls. and (current bias\$5) same (impedance resist\$5) same transistor same (resistor impedance) same ((first one) and (second other))	USPAT	OR	ON	2005/11/11 13:48
L32	13	360/60,61,66,67,68.ccls. and (current bias\$5) same (impedance resist\$5) same transistor same (resistor impedance) same (first one) same buffer\$5 and (second other) same (current bias\$5) same (impedance resist\$5) same transistor same (resistor impedance) same buffer\$5	USPAT	OR	ON	2005/11/11 13:52
L33	49	360/60,61,66,67,68.ccls. and (current bias\$5) same (impedance resist\$5) same transistor same (resistor impedance) same buffer\$5	USPAT	OR	ON	2005/11/11 13:52
L34	36	I33 not I32	USPAT	OR	ON	2005/11/11 13:52